



PATENT
Attorney Docket No. QCS-001DV3

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT(S): Kamieniecki et al.
SERIAL NO.: 09/932,754 GROUP NO.: 2829
FILING DATE: August 17, 2001 EXAMINER: J. Hollington
TITLE: Real-Time In-Line Testing of Semiconductor Wafers

Mail Stop Non-Fee Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT AND RESPONSE

Sir:

In response to the Office Action mailed on February 25, 2004, in connection with the patent application identified above, for which the period of response ends on May 25, 2004, Applicants respectfully request entry of this response, reconsideration, and passage of this application to allowance.

Amendments to the Claims are reflected in the listing of claims that begins on page 2 of this paper.

The **Response** begins on page 4 of this paper.

The **Conclusion** begins on page 8 of this paper.